

Final term of cs401

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Question No: 1 (Marks: 1) - Please choose one

SP is associated with..... By default

▶ **SS** page 26

▶ DS

▶ CS

▶ ES

Question No: 2 (Marks: 1) - Please choose one

Which bit of the attributes byte represents the red component of foreground color

▶ 5

▶ 4

▶ 3

▶ **2** page73

Question No: 3 (Marks: 1) - Please choose one

An 8 x 16 font is stored in _____ bytes.

▶ 2

▶ 4

▶ 8

▶ **16** page142

Question No: 4 (Marks: 1) - Please choose one

In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

- ▶ third
- ▶ fourth
- ▶ first
- ▶ **second** [page 144](#)

Question No: 5 (Marks: 1) - Please choose one

Which of the following gives the more logical view of the storage medium

- ▶ BIOS
- ▶ **DOS** [153page](#)
- ▶ Both
- ▶ None

Question No: 6 (Marks: 1) - Please choose one

In STOSW instruction, when DF is clear, SI is

- ▶ Incremented by 1
- ▶ **Incremented by 2**
- ▶ Decrement by 1
- ▶ Decrement by 2

Question No: 7 (Marks: 1) - Please choose one

Which of the following interrupts is Non maskable interrupt

▶ **INT 2** PAGE 97

▶ INT 3

▶ INT 0

▶ INT 1

Question No: 8 (Marks: 1) - Please choose one

Which of the following IRQs is connected to serial port COM 2?

▶ IRQ 0

▶ IRQ 1

▶ IRQ 2

▶ **IRQ 3** PAGE 106

Question No: 9 (Marks: 1) - Please choose one

The time interval between two timer ticks is ?

▶ 40ms

▶ 45ms

▶ 50ms

▶ **55ms** PAGE 114

Question No: 10 (Marks: 1) - Please choose one

The physical address of IDT(Interrupt Descriptor Table) is stored in _____

▶ GDTR

▶ **IDTR** PAGE 174

▶ IVT

► IDTT

Question No: 11 (Marks: 1) - Please choose one

In NASM an imported symbol is declared with the while and exported symbol is declared with the

- Global directive, External directive
- **External directive, Global directive PAGE 181**
- Home Directive, Foreign Directive
- Foreign Directive, Home Directive

Question No: 12 (Marks: 1) - Please choose one

In 68K processors there is a 32bit that holds the address of currently executing instruction

- Program counter PAGE 184
- Stack pointer
- **Register**
- Stack

Question No: 13 (Marks: 1) - Please choose one

Single step interrupt is

- Hardware interrupt
- **Like divide by zero interrupt**
- Like divide by 1 interrupt
- Software interrupt

Question No: 14 (Marks: 1) - Please choose one

Which of the following is NOT true about registers:

- Their operation is very much like memory
- Intermediate results may also be stored in registers

▶ They are also called scratch pad ram

▶ **None of given options**

CURRENT

Question No: 15 (Marks: 1) - Please choose one

Types of jump are:

▶ short, near

▶ **short, near, far PAGE 37**

▶ near, far

▶ short, far

Question No: 16 (Marks: 1) - Please choose one

MS DOS uses ____ display mode.

▶ **Character based** page 72

▶ Graphics based

▶ Numeric based

▶ Console based

Question No: 17 (Marks: 1) - Please choose one

Which of the following IRQs is derived by a timer device?

▶ **IRQ 0** page 106

▶ IRQ 1

▶ IRQ 2

▶ IRQ 3

Question No: 18 (Marks: 1) - Please choose one

In programmable interrupt controller, which of the following ports is referred as a *control port*.

▶ 19

▶ 20 page 107

▶ 21

▶ 22

Question No: 19 (Marks: 1) - Please choose one

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

▶ AL page 144

▶ BL

▶ CL

▶ BH

Question No: 20 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to DSR (DataSet Ready) ?

▶ 4

▶ 5

▶ 6 page 146

▶ 7

Question No: 21 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to TD (Transmitted Data) ?

▶ 1

▶ 2

▶ 3 page 146

▶ 4

Question No: 22 (Marks: 1) - Please choose one

In 9pin DB 9, Signal ground is assigned on pin number

- ▶ 4
- ▶ **5** page 146
- ▶ 6
- ▶ 3

Question No: 23 (Marks: 1) - Please choose one

8088 is a

- ▶ **16 bit processor** page 6
- ▶ 32 bit processor
- ▶ 64 bit processor
- ▶ 128 bit processor

Question No: 24 (Marks: 1) - Please choose one

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

- ▶ 1
- ▶ **0** page 168
- ▶ -1
- ▶ -2

Question No: 25 (Marks: 1) - Please choose one

VESA(Video Electronics Standards Association) organizes 16 color bits for every pixel in

- ▶ 5:5:5 format
- ▶ **5:6:5 format** page 172
- ▶ 6:5:6 format
- ▶ 5:6:7 format

Question No: 26 (Marks: 1) - Please choose one

Which flags are NOT used for mathematical operations ?

- ▶ Carry, Interrupt and Trap flag.
- ▶ **Direction, Interrupt and Trap flag.** Page 126
- ▶ Direction, Overflow and Trap flag.
- ▶ Direction, Interrupt and Sign flag.

Question No: 1 (Marks: 1) - Please choose one

Suppose AL contains 5 decimal then after two left shifts produces the value as

- ▶ **5**
- ▶ 10
- ▶ 15
- ▶ 20

Question No: 2 (Marks: 1) - Please choose one

In graphics mode a location in video memory corresponds to a _____ on the screen.

- ▶ line
- ▶ **dot** page 142
- ▶ circle
- ▶ rectangle

Question No: 3 (Marks: 1) - Please choose one

Creation of threads can be

- ▶ static
- ▶ **dynamic** page 133
- ▶ easy

► difficult

Question No: 4 (Marks: 1) - Please choose one

The thread registration code initializes the PCB and adds it to the linked list so that the _____ will give it a turn.

- assembler
- [scheduler page 133](#)
- linker
- debugger

Question No: 5 (Marks: 1) - Please choose one

VESA VBE 2.0 is a standard for

- [High resolution Mode page 172](#)
- Low resolution Mode
- Medium resolution Mode
- Very High resolution Mode

Question No: 7 (Marks: 1) - Please choose one

Which of the following IRQs is derived by a key board?

- IRQ 0
- [IRQ 1 page106](#)
- IRQ 2
- IRQ 3

Question No: 8 (Marks: 1) - Please choose one

Which of the following IRQs is used for Floppy disk derive?

- IRQ 4

▶ IRQ 5

▶ **IRQ 6** page 106

▶ IRQ 7

Question No: 9 (Marks: 1) - Please choose one

Which of the following pins of a parallel port connector are grounded?

▶ 10-18

▶ **18-25** 117page

▶ 25-32

▶ 32-39

Question No: 10 (Marks: 1) - Please choose one

The physical address of IDT(Interrupt Descriptor Table) is stored in _____

▶ GDTR

▶ **IDTR** 174page

▶ IVT

▶ IDTT

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In NASM an imported symbol is declared with the while and exported symbol is declared with the

▶ Global directive, External directive

▶ **External directive, Global directive** 181pages

▶ Home Directive, Foreign Directive

▶ Foreign Directive, Home Directive

Question No: 12 (Marks: 1) - Please choose one

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction

- ▶ 8bit
- ▶ 16bit
- ▶ **32bit** 183page
- ▶ 64bit

Question No: 13 (Marks: 1) - Please choose one

To reserve 8-bits in memory ____ directive is used.

- ▶ **db** 17page
- ▶ dw
- ▶ dn
- ▶ dd

For single byte we use db and for two bytes we use dw.

Question No: 14 (Marks: 1) - Please choose one

In the “mov ax, 5” 5 is the _____ operand.

- ▶ **source**
- ▶ destination Page 18
- ▶ memory
- ▶ register

Question No: 15 (Marks: 1) - Please choose one

RETf will pop the segment address in the

- ▶ **CS register page 61**
- ▶ DS register
- ▶ SS register
- ▶ ES register

Question No: 16 (Marks: 1) - Please choose one

For the execution of the instruction “DIV BL”, the implied dividend will be stored in

▶ **AX**

▶ BX

▶ CX

▶ DX

Question No: 17 (Marks: 1) - Please choose one

When a number is divided by zero "A Division by 0" interrupt is generated. Which instruction is used for this purpose

▶ INT 0

▶ INT 1

▶ INT 2

▶ **This interrupt is generated automatically**

Question No: 18 (Marks: 1) - Please choose one

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

▶ **AL**

▶ BL

▶ CL

▶ BH

Question No: 19 (Marks: 1) - Please choose one

BIOS sees the disks as

▶ logical storage

▶ **raw storage page 148**

▶ in the form of sectors only

▶ in the form of tracks only

Question No: 20 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to CD (Carrier Detect) ?

- ▶ 1
- ▶ 2
- ▶ 3
- ▶ 4

Question No: 21 (Marks: 1) - Please choose one

In 9pin DB 9, Signal ground is assigned on pin number

- ▶ 4
- ▶ 5
- ▶ 6
- ▶ 3

Question No: 22 (Marks: 1) - Please choose one

In 9pin DB 9, RI (Ring Indicator) is assigned on pin number

- ▶ 6
- ▶ 7
- ▶ 8
- ▶ 9

Question No: 23 (Marks: 1) - Please choose one

Motorola 68K processors have 23bit general purpose registers.

- ▶ 4
- ▶ 8
- ▶ 16
- ▶ 32

Question No: 24 (Marks: 1) - Please choose one

When two devices in the system want to use the same IRQ line then what will happen?

▶ An IRQ Collision

▶ **An IRQ Conflict** 106page

▶ An IRQ Crash

▶ An IRQ Blockage

Question No: 25 (Marks: 1) - Please choose one

In the instruction **MOV AX, 5** the number of operands are

▶ 1

▶ **2**

▶ 3

▶ 4

Question No: 26 (Marks: 1) - Please choose one

Which flags are NOT used for mathematical operations ?

▶ Carry, Interrupt and Trap flag.

▶ **Direction, Interrupt and Trap flag.** 125page

▶ Direction, Overflow and Trap flag.

▶ Direction, Interrupt and Sign flag.

Question No: 1 (Marks: 1) - Please choose one

Suppose AL contains 5 decimal then after two left shifts produces the value as

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- ▶ 10
- ▶ 15
- ▶ 20

Question No: 2 (Marks: 1) - Please choose one

In graphics mode a location in video memory corresponds to a _____ on the screen.

- ▶ line
- ▶ dot 141page
- ▶ circle
- ▶ rectangle

Question No: 3 (Marks: 1) - Please choose one

Creation of threads can be

- ▶ static
- ▶ dynamic 133PAGE
- ▶ easy
- ▶ difficult

Question No: 4 (Marks: 1) - Please choose one

The thread registration code initializes the PCB and adds it to the linked list so that the _____ will give it a turn.

- ▶ assembler
- ▶ scheduler 133PAGE
- ▶ linker
- ▶ debugger

Question No: 5 (Marks: 1) - Please choose one

VESA VBE 2.0 is a standard for

- ▶ High resolution Mode 172PAGE
- ▶ Low resolution Mode
- ▶ Medium resolution Mode
- ▶ Very High resolution Mode

Question No: 7 (Marks: 1) - Please choose one

Which of the following IRQs is derived by a key board?

- ▶ IRQ 0
- ▶ IRQ 1 106PAGE
- ▶ IRQ 2
- ▶ IRQ 3

Question No: 8 (Marks: 1) - Please choose one

Which of the following IRQs is used for Floppy disk derive?

- ▶ IRQ 4
- ▶ IRQ 5
- ▶ IRQ 6 106PAGE
- ▶ IRQ 7

Question No: 14 (Marks: 1) - Please choose one

In the “mov ax, 5” 5 is the _____ operand.

- ▶ **source 16page**
- ▶ destination
- ▶ memory
- ▶ register

Question No: 15 (Marks: 1) - Please choose one

RETF will pop the segment address in the

- ▶ **CS register**
- ▶ DS register
- ▶ SS register
- ▶ ES register

Question No: 16 (Marks: 1) - Please choose one

For the execution of the instruction “DIV BL”, the implied dividend will be stored in

- ▶ **AX**
- ▶ BX
- ▶ CX
- ▶ DX

Question No: 17 (Marks: 1) - Please choose one

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- ▶ INT 0
- ▶ INT 1
- ▶ INT 2
- ▶ **This interrupt is generated automatically**

Question No: 18 (Marks: 1) - Please choose one

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

- ▶ **AL**
- ▶ BL
- ▶ CL
- ▶ BH

Question No: 19 (Marks: 1) - Please choose one

BIOS sees the disks as

- ▶ logical storage
- ▶ **raw storage 148page**
- ▶ in the form of sectors only
- ▶ in the form of tracks only

Question No: 20 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to CD (Carrier Detect) ?

- ▶ **1 104page**
- ▶ 2

- ▶ 3
- ▶ 4

Question No: 21 (Marks: 1) - Please choose one

In 9pin DB 9, Signal ground is assigned on pin number

- ▶ 4
- ▶ 5 104page
- ▶ 6
- ▶ 3

Question No: 22 (Marks: 1) - Please choose one

In 9pin DB 9, RI (Ring Indicator) is assigned on pin number

- ▶ 6
- ▶ 7
- ▶ 8
- ▶ 9 104page

Question No: 23 (Marks: 1) - Please choose one

Motorola 68K processors have 23bit general purpose registers.

- ▶ 4
- ▶ 8
- ▶ 16 183page
- ▶ 32

Question No: 25 (Marks: 1) - Please choose one

In the instruction **MOV AX, 5** the number of operands are

- ▶ 1
- ▶ 2
- ▶ 3
- ▶ 4

Question No: 1 (Marks: 1) - Please choose one

Which of the following is not true about registers:

- Their operation is very much like memory
- Intermediate results may also be stored in registers.
- They are also called scratch pad ram
- None of given options.

Question No: 2 (Marks: 1) - Please choose one

move [bp], al moves the one byte content of the AL register to the address contained in BP register in the current

- Stack segment 27page
- Code segment
- Data segment
- Extra segment

Question No: 3 (Marks: 1) - Please choose one

In a rotate through carry right (RCR) instruction applied on a 16 bit word effectively there is

- 16 bits rotation
- 1 bit rotation

- 17 bits rotation **45page**
- 8 bits rotation

Question No: 4 (Marks: 1) - Please choose one
The 8088 stack works on

- Word sized elementsn 60page
- Byte sized elements
- Double sized element
- Nible sized element

1
Question No: 5 (Marks: 1) - Please choose one
An 8 x 16 font is stored in _____ bytes.

- 2
- 4
- 8
- 16 **142page**

4
Question No: 6 (Marks: 1) - Please choose one
INT 10 is used for _____ services.

- RAM
- Disk
- BIOS video **141PAGE**
- DOS video

Question No: 7 (Marks: 1) - Please choose one
Priority of IRQ 0 interrupt is

- medium
- high
- highest **106PAGE**
- low

3
Question No: 8 (Marks: 1) - Please choose one
Threads can have function calls, parameters and _____ variables.

- global
- local **133PAGE**
- legal
- illegal

2
Question No: 9 (Marks: 1) - Please choose one
How many prevalent calling conventions do exist

- 1
 - 2 **180PAGE**
 - 3
 - 4
- Two prevalent calling conventions are the C calling convention and the Pascal calling convention

Question No: 10 (Marks: 1) - Please choose one
In 9pin DB 9 DSR is assigned on pin number

- 4
- 5
- 6 **164PAGE**
- 7

Question No: 11 (Marks: 1) - Please choose one
In 9pin DB 9 CTS is assigned on pin number

- 6
- 7
- 8 **164PAGE**
- 9

Question No: 12 (Marks: 1) - Please choose one
In 9pin DB 9 CD is assigned on pin number

- 1 **164PAGE**
- 2
- 3
- 4

Question No: 13 (Marks: 1) - Please choose one
In 9pin DB 9 RD is assigned on pin number

- 1
- 2 **164PAGE**
- 3
- 4

Question No: 14 (Marks: 1) - Please choose one
in device attribute word which of the following bit decides whether it is a charater device or a block device

- Bit 12
- Bit 13
- Bit 14
- Bit 15 **158PAGE**

Question No: 15 (Marks: 1) - Please choose one
Video servioces are classified into _____ broad catagories

- 2 **141PAGE**
- 3
- 4
- 5

1
Question No: 16 (Marks: 1) - Please choose one
In STOSB instruction, when DF is clear, SI is

- Incremented by 1
- Incremented by 2
- Decrementd by 1
- Decrementd by 2

1
Question No: 17 (Marks: 1) - Please choose one
The process of sending signals back and forth is called

- Activity
- Hand-shaking
- Interruption
- Time clicking

Question No: 18 (Marks: 1) - Please choose one
Which of the following interrupts is used for Arithmetic overflow

- INT 1
- INT 2
- INT 3
- INT 4 **98page**

4

Question No: 19 (Marks: 1) - Please choose one
which of the following is a special type of interrupt that returns to the same instruction instead of the next instruction

- Divide overflow interrupt **99page**
- Debug interrupt
- Arithmetic overflow interrupt
- Change of sign interrupt

Question No: 20 (Marks: 1) - Please choose one
Which of the following IRQs is derived by a timer device?

- IRQ 0 **106page**
- IRQ 1
- IRQ 2
- IRQ 3

1

Question No: 21 (Marks: 1) - Please choose one
Which of the following IRQs is connected to serial port COM 2?

- IRQ 0
- IRQ 1
- IRQ 2
- IRQ 3 **106page**

4

Question No: 22 (Marks: 1) - Please choose one
An End of Interrupt (EOI) signal is sent by

- Handler
- Processor
- IRQ
- PIC **106page**

4

Question No: 23 (Marks: 1) - Please choose one
The source registers in OUT is

- AL or AX **107page**
- BL or BX
- CL or CX
- DL or DX

1

Question No: 24 (Marks: 1) - Please choose one
In programmable interrupt controller which of the following ports is used for selectively enabling or disabling interrupts

- 19
- 20
- 21 **107page**
- 22

3

Question No: 25 (Marks: 1) - Please choose one
The number of pins in a parallel port connector are?

- 20

- 25 **117page**
- 30
- 35

2

Question No: 27 (Marks: 1) - Please choose one

Suppose a decimal number 35 when its binary is shifted to write two places the new number will become

- 35
- 70
- 140
- 17

Question No: 28 (Marks: 1) - Please choose one

A 32bit address register can access upto of memory so memory access has increased a lot.

- 2GB
- 4GB **168page**
- 6GB
- 8GB

Question No: 1 (Marks: 1) - Please choose one

The physical address of the stack is obtained by

- ▶ SS:SI combination
- ▶ SS:SP combination
- ▶ ES:BP combination
- ▶ ES:SP combination

2

Question No: 2 (Marks: 1) - Please choose one

Value of AH in the write Graphics pixel service is

- ▶ 0Ch
- ▶ 0Bh
- ▶ 1Ch
- ▶ 2Ch

1

Question No: 3 (Marks: 1) - Please choose one

Threads can have function calls, parameters and _____ variables.

- ▶ global
- ▶ local
- ▶ legal
- ▶ illegal

2

Question No: 4 (Marks: 1) - Please choose one

Creation of threads can be

- ▶ static
- ▶ dynamic
- ▶ easy
- ▶ difficult

2

Question No: 5 (Marks: 1) - Please choose one

How many prevalent calling conventions do exist

- ▶ 1
- ▶ 2

- ▶ 3
- ▶ 4

2

Question No: 6 (Marks: 1) - Please choose one
VESA VBE 2.0 is a standard for

- ▶ High resolution Mode
- ▶ Low resolution Mode
- ▶ Medium resolution Mode
- ▶ Very High resolution Mode

1

Question No: 7 (Marks: 1) - Please choose one
The serial port connection is a ----- connector

- ▶ 9pin DB 9
- ▶ 8pin DB 9
- ▶ 3pin DB 9
- ▶ 9pin DB 5

1

Question No: 8 (Marks: 1) - Please choose one
Which of the following gives the more logical view of the storage medium

- ▶ BIOS
- ▶ DOS
- ▶ Both
- ▶ None

2

Question No: 9 (Marks: 1) - Please choose one
In STOSB instruction, when DF is clear, SI is

- ▶ Incremented by 1
- ▶ Incremented by 2
- ▶ Decrement by 1
- ▶ Decrement by 2

1

Question No: 10 (Marks: 1) - Please choose one
After the execution of STOSW the CX will be

- ▶ Decrement by 1
- ▶ Decrement by 2
- ▶ Increment by 1
- ▶ Increment by 2

1

Question No: 11 (Marks: 1) - Please choose one
IRQ is referred to

- ▶ Eight input signals
- ▶ One output signal
- ▶ One input signals
- ▶ Eight output signals

1

Question No: 12 (Marks: 1) - Please choose one

Which of the following IRQs is derived by a key board?

- ▶ IRQ 0
- ▶ IRQ 1
- ▶ IRQ 2
- ▶ IRQ 3

2

Question No: 13 (Marks: 1) - Please choose one

Which of the following IRQs is connected to serial port COM 1?

- ▶ IRQ 4
- ▶ IRQ 5
- ▶ IRQ 6
- ▶ IRQ 7

1

Question No: 14 (Marks: 1) - Please choose one

The physical address of IDT(Interrupt Descriptor Table) is stored in _____

- ▶ GDTR
- ▶ IDTR
- ▶ IVT
- ▶ IDTT

2

Question No: 15 (Marks: 1) - Please choose one

Assembly language is:

- ▶ Low-level programming language
- ▶ High-level programming language
- ▶ Also known as machine language
- ▶ Not considered closer to the computer

1

Question No: 16 (Marks: 1) - Please choose one

The number of bits required to access 1MB of memory are

- ▶ 16 bits
- ▶ 32 bits
- ▶ Depends on the processor architecture
- ▶ 20 bits

4

Question No: 17 (Marks: 1) - Please choose one

In STOSB instruction, SI is decremented or incremented by

- ▶ 3
- ▶ 2
- ▶ 1
- ▶ 4

3

Question No: 18 (Marks: 1) - Please choose one

In programmable interrupt controller, which of the following ports is referred as a control port.

- ▶ 19
- ▶ 20
- ▶ 21
- ▶ 22

3

Question No: 19 (Marks: 1) - Please choose one

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

- ▶ AL
- ▶ BL
- ▶ CL
- ▶ BH

1

Question No: 20 (Marks: 1) - Please choose one

In device attribute word, which of the following bit decides whether it is a character device or

a block device

- ▶ Bit 12
- ▶ Bit 13
- ▶ Bit 14
- ▶ Bit 15

4

Question No: 21 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to CTS (Clear To Send) ?

- ▶ 6
- ▶ 7
- ▶ 8
- ▶ 9

3

Question No: 22 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to RD (Received Data) ?

- ▶ 1
- ▶ 2
- ▶ 3
- ▶ 4

2

Question No: 23 (Marks: 1) - Please choose one

VESA(Video Electronics Standards Association) organizes 16 color bits for every pixel in

- ▶ 5:5:5 format
- ▶ 5:6:5 format
- ▶ 6:5:6 format
- ▶ 5:6:7 format

2

Question No: 24 (Marks: 1) - Please choose one

Motorola 68K processors have 23bit general purpose registers.

- ▶ 4
- ▶ 8
- ▶ 16
- ▶ 32

3

Question No: 25 (Marks: 1) - Please choose one

Programmable Interrupt Controller (PIC) has

- ▶ One input signals and eight output signals
- ▶ One input signal and one output signal
- ▶ Eight input signals and one output signals
- ▶ Eight input signals and eight output signals

3

Question No: 26 (Marks: 1) - Please choose one

Video services are classified into..... broad categories.

- ▶ 5
- ▶ 4
- ▶ 3
- ▶ 2

4